

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Tsuyoshi KANEKO

Application No.: 10/849,884

Docket No.: 119593

Filed: May 21, 2004

For: BUMP STRUCTURE AND METHOD OF MANUFACTURING THE SAME, AND  
MOUNTING STRUCTURE FOR IC CHIP AND CIRCUIT BOARD

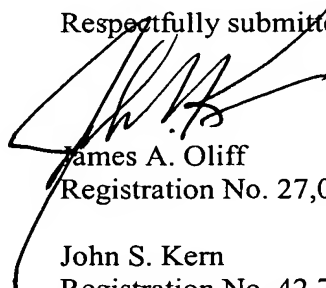
**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



James A. Oliff  
Registration No. 27,075

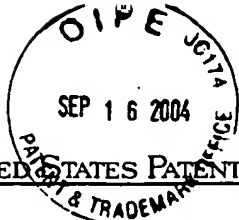
John S. Kern  
Registration No. 42,719

JAO:JSK/kap

Date: September 16, 2004

**OLIFF & BERRIDGE, PLC**  
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<p><b>DEPOSIT ACCOUNT USE AUTHORIZATION</b> Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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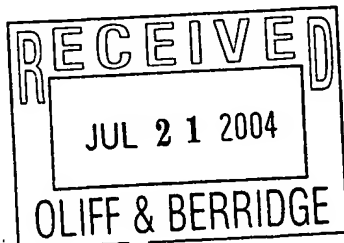


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/849,884	05/21/2004	2811	1028	119593	9	20	6

25944  
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CONFIRMATION NO. 8590

## FILING RECEIPT



\*OC000000013268803\*

Date Mailed: 07/19/2004

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## Applicant(s)

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Tsuyoshi Kaneko, ~~Suwa~~-shi, JAPAN;

## Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-169051 06/13/2003

If Required, Foreign Filing License Granted: 07/19/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Bump structure and method of manufacturing the same, and mounting structure for IC chip and circuit board

## Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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